This listing of claims will replace all prior versions, and listings, of claims in the application:

1-86. (Canceled).

87. (Currently Amended) A semiconductor device comprising:

a first thin film transistor provided in a matrix pixel circuit over a substrate; and

a second thin film transistor provided in a peripheral driving circuit over said substrate, each of said first and second thin film transistors comprising:

a crystalline semiconductor island;

source and drain regions in said crystalline semiconductor island;

a channel forming region between said source and drain regions;

a gate insulating film adjacent to at least said channel forming region; and

a gate electrode adjacent to said channel forming region having said gate insulating film therebetween,

wherein each of said crystalline semiconductor islands of said first and second thin film transistors is formed in a monodomain region which contains no grain boundary,

wherein at least one of hydrogen and halogen element is contained at \underline{a} concentration not higher than 1 x 10^{20} cm⁻³ in said monodomain regions of said first and second thin film transistors,

wherein each of said crystalline semiconductor island islands of said first and second thin film transistor transistors includes a nickel at a concentration of 1×10^{17} cm⁻³ to 5×10^{17} cm⁻³.

wherein said crystalline semiconductor island of said second thin film transistor includes nickel at a concentration of 1 x 10^{16} cm⁻³ or less, and

wherein a concentration of said nickel in said crystalline semiconductor island of said first thin film transistor is smaller than that of said nickel in said crystalline semiconductor island of said second thin film transistor.

88. (Previously Presented) A device according to claim 87, wherein each of said crystalline semiconductor islands of said first and second thin film transistors comprises a material selected from the group consisting of Fe, Co, Ni, Ru, Rh, Pd, Os, Ir, Pt, Cu, Ag, Au.

89. (Canceled)

- 90. (Previously Presented) A device according to claim 87, wherein each of said crystalline semiconductor islands of said first and second thin film transistors is a silicon island.
- 91. (Previously Presented) A device according to claim 87, wherein each of said crystalline semiconductor islands of said first and second thin film transistors includes carbon and nitrogen at a concentration not lower than 1×10^{16} cm⁻³, and oxygen at a concentration not lower than 1×10^{17} cm⁻³.
- 92. (Currently Amended) A device according to claim 87, wherein said monodomain regions of said first and second thin film transistors have [[has]] a grain size of 50 µm or more.

93-122. (Canceled)

- 123. (Currently Amended) A semiconductor device comprising:
- a first thin film transistor provided in a matrix pixel circuit over a substrate; and
- a second thin film transistor provided in a peripheral driving circuit over said substrate, each of said first and second thin film transistors comprising:
 - a crystalline semiconductor island;
 - source and drain regions in said crystalline semiconductor island;
 - a channel forming region between said source and drain regions;
 - a gate insulating film adjacent to at least said channel forming region; and
 - a gate electrode adjacent to said channel forming region having said gate insulating film therebetween,

wherein each of said crystalline semiconductor islands of said first and second thin film transistors includes carbon and nitrogen at a concentration not higher than 5×10^{18} cm⁻³,

wherein each of said crystalline semiconductor islands of said first and second thin film transistors is formed in a monodomain region which contains no grain boundary,

wherein said semiconductor device has a S value of 0.03 0.3,

wherein each of said crystalline semiconductor islands of said first and second thin film transistors includes at least one of hydrogen and halogen element at \underline{a} concentration not higher than 1×10^{20} cm⁻³ in said monodomain region,

wherein each of said crystalline semiconductor <u>island</u> islands of said first and second thin film <u>transistor</u> transistors includes a nickel at a concentration of 1×10^{17} cm⁻³ to 5×10^{17} cm⁻³,

wherein said crystalline semiconductor island of said second thin film transistor includes nickel at a concentration of 1×10^{16} cm⁻³ or less, and

wherein a concentration of said nickel in said crystalline semiconductor island of said first thin film transistor is smaller than that of said nickel in said crystalline semiconductor island of said second thin film transistor.

124. (Previously Presented) A device according to claim 123, wherein each of said crystalline semiconductor islands of said first and second thin film transistors comprises a material selected from the group consisting of Fe, Co, Ni, Ru, Rh, Pd, Os, Ir, Pt, Cu, Ag, Au.

125. (Canceled)

126. (Previously Presented) A device according to claim 123, wherein each of said crystalline semiconductor islands of said first and second thin film transistors is a silicon island.

- 127. (Previously Presented) A device according to claim 123, wherein each of said crystalline semiconductor islands of said first and second thin film transistors includes carbon and nitrogen at a concentration not lower than 1×10^{16} cm⁻³, and oxygen at a concentration not lower than 1×10^{17} cm⁻³.
- 128. (Currently Amended) A device according to claim 123, wherein said monodomain regions of said first and second thin film transistors have [[has]] a grain size of 50 µm or more.

129-136. (Canceled)

Page 5

137. (Previously Presented) A device according to claim 91, wherein each of the concentrations of carbon, nitrogen and oxygen is measured by secondary ion mass

spectroscopy (SIMS).

138-142. (Canceled).

143. (Previously Presented) A device according to claim 127, wherein each of the

concentrations of carbon, nitrogen and oxygen is measured by secondary ion mass

spectroscopy (SIMS).

144-148. (Canceled)

149. (Currently Amended) The semiconductor device according to claim 87 wherein

said crystalline semiconductor islands of said first and second thin film transistors include

includes carbon and nitrogen at a concentration not higher than 5 x 10¹⁸ cm⁻³, and oxygen at a

concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$.

150-155. (Canceled)